

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Changming Jin et al.

Docket Number: TI-26111

Serial No.: 09/178,249

Examiner: G. Lee

Filed: 10/23/1998

Art Unit: 2825

11/GAU-875/\$
#5
Scripps
7/20/00

Integrated Circuit Dielectric and Method

NOTICE OF APPEAL



Assistant Commissioner
for Patents
Washington, DC 20231

MAILING CERTIFICATE	
I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, DC 20231 today.	
<u>Cecelia Grant</u>	<u>7-13-00</u>
	Date

Dear Sir:

Applicant hereby appeals to the Board of Appeals from the decision of the Primary Examiner dated **04/13/2000** all rejected claims.

The item(s) checked below are appropriate:

1. _____ An extension of time to respond to the final rejection
_____ was granted on
_____ is requested for month(s).
2. _____ A timely response to the final rejection has been filed.
3. X Fee \$300.00:
_____ Not required (Fee paid in prior appeal).

RECEIVED
JUL 19 2000
TECHNOLOGY CENTER 2800

07/18/2000 SBUONG 00000098 200668 09178249
01 FC:119 300.00 CH

 X The Commissioner is hereby authorized to charge any fees which may be required, or credit any overpayment to the Deposit Account of Texas Instruments Incorporated, Account No. 20-0668. An original and one copy of this sheet are enclosed.

Signature [Rule 191(b)]

Carlton H. Hoel
Registration No. 29,934
Texas Instruments Incorporated
PO Box 655474, M/S 3999
Dallas, TX 75265
(972) 917-4365

Post Office Address
(to which correspondence is to be sent)